



(0) (data adj processing a...)

(422) (data adj processing adj device or microcomputer) and (substrate and processor and mem...)

(56) (data adj processing) DBs: USPAT, EPO, JPO ☒ Plurals

(0) scaleable adj integrat... Default operator: OR ☒ Highlight all hit terms initially

(0) (integrated adj data a...)

(4) (integrated adj data a... (substrate and processor and memory and memory adj bus\$2 and layer\$1)

(13) (integrated adj data a...)

(671) data adj processing

BRS f... IS&R... Image Text HTML

U	1	Document ID	Issue Date	Pages	Title	Current OR	Current XRef
<input type="checkbox"/>	<input type="checkbox"/>	US 6414368 B1	20020702	41	Microcomputer with high density RAM on single chip	257/523	712/10; 712/11;
<input type="checkbox"/>	<input type="checkbox"/>	US 6345010 B1	20020205	27	Semiconductor storage device	365/230.03	365/207; 365/69
<input type="checkbox"/>	<input type="checkbox"/>	US 6298370 B1	20011002	173	Computer operating process allocating tasks between	709/102	709/100
<input type="checkbox"/>	<input type="checkbox"/>	US 6179489 B1	20010130	173	Devices, methods, systems and software products for	709/102	
<input type="checkbox"/>	<input type="checkbox"/>	US 6168973 B1	20010102	25	Semiconductor stacked device for implantable medical	438/109	438/612; 438/613
<input type="checkbox"/>	<input type="checkbox"/>	US 6167466 A	20001226	51	Multi-channel serial port with programmable features	710/30	709/232; 709/236;
<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6105119 A	20000815	188	Data transfer circuitry, DSP wrapper circuitry and	711/219	710/110
<input type="checkbox"/>	<input type="checkbox"/>	US 6094715 A	20000725	66	SIMD/MIMD processing synchronization	712/20	712/303
<input type="checkbox"/>	<input type="checkbox"/>	US 6051887 A	20000418	22	Semiconductor stacked device for implantable medical	257/777	257/786
0	<input type="checkbox"/>	US 5974493 A	19991026	50	Microcomputer with processor bus having smaller width	710/307	710/66
1	<input type="checkbox"/>	US 5966528 A	19991012	67	SIMD/MIMD array processor with vector processing	712/222	712/10; 712/203;

Hits Details HTML

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Active

L1: (116) integrated adj d.

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(0) scaleable adj integrat.

(103325) data adj processi

(200177) data adj processi

(1239) (data adj processin

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DBs USPAT:EPD:JPO

☒ Plurals☒ Highlight all hit terms initially

Default operator: OR

integrated adj data adj processing

BRS f... IS&R... Image Text HTML

	U	I	Document ID	Issue Date	Pages	Title	Current OR	Current XRef
1	<input type="checkbox"/>	<input type="checkbox"/>	US 6468800 B1	20021022	55	Systems and methods for rapidly identifying useful	436/43	422/100;
2	<input type="checkbox"/>	<input type="checkbox"/>	US 6456989 B1	20020924	180	Neuro-fuzzy-integrated data processing system	706/2	422/63;
3	<input type="checkbox"/>	<input type="checkbox"/>	US 6449545 B1	20020910	8	Method for data transfer in a restraint system connected	701/45	706/4;
4	<input type="checkbox"/>	<input type="checkbox"/>	US 6449063 B1	20020910	31	Facsimile apparatus	706/47;	711/33
5	<input type="checkbox"/>	<input type="checkbox"/>	US 6434490 B1	20020813	30	Method of generating chemical compounds having	358/400	358/435;
6	<input type="checkbox"/>	<input type="checkbox"/>	US 6421612 B1	20020716	55	System, method and computer program product for	702/27	399/3;
7	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6412038 B1	20020625	13	Integral modular cache for a processor	702/19	435/69.1;
8	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6407827 B1	20020618	10	Data communicating apparatus capable of storing a	702/19	435/7.24;
9	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6356761 B1	20020312	15	Method and arrangement for finding information	702/19	365/51;
10	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6354858 B1	20020312	7	Retention apparatus for flash memory card	358/404	711/128;
11	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6301263 B1	20011099	12	Method and apparatus for providing fair access in a	455/456	358/405